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E-MAIL: MAIL@MILLSONELLO.COM**FACSIMILE TRANSMISSION****TO: United States Patent and Trademark Office FROM: Steven M. Mills****FAX NO: (703) 872-9306****DATE: March 29, 2004****RE: U.S. Patent Application
"Method of Manufacturing Interconnection Line in Semiconductor Device,"
by Soo-geun Lee, et al.
U.S. Serial No.: 10/081,661
U.S. Filing Date: February 22, 2002
Our Reference No.: SAM-0313****NUMBER OF PAGES INCLUDING COVER SHEET: 2**

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PATENT
 Attorney Docket No.: SAM-0313
 Customer No.: 29344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Soo-geun Lee, *et al.* Examiner: Nguyen, H.
 Serial No.: 10/081,661 Group Art Unit: 2812
 Filed: February 22, 2002
 Title: METHOD OF MANUFACTURING INTERCONNECTION LINE IN SEMICONDUCTOR DEVICE

CERTIFICATE OF FACSIMILE TRANSMISSION

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Lisa Sanders

Lisa Sanders

Commissioner for Patents
 P.O. Box 1450
 Alexandria, Virginia 22313-1450

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REQUEST FOR CONSIDERATION OF
 INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

Upon review of the Notice of Allowance that was mailed on March 17, 2004, it is noted that the Supplemental Information Disclosure Statement that was filed on February 27, 2004 was not acknowledged by the Examiner. It is requested that the Examiner acknowledge that the information cited in the Supplemental IDS has been considered and made of record in the application.

This paper is being filed in response to the Examiner's request, made during a telephone conversation on March 29, 2004 between the Examiner and Ms. Lisa Sanders, the patent administrator in the office of the undersigned.

Thank you for your assistance.

Respectfully submitted,

Steven M. Mills
 Steven M. Mills
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 Attorney for Applicants

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